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Sheet	1	of	2
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**Complete if Known**

Application Number	10/612,333
Filing Date	July 2, 2003
First Named Inventor	Luan Tran
Art Unit	
Examiner Name	
Attorney Docket Number	MIO 0033 V2/40509.257

## U. S. PATENT DOCUMENTS

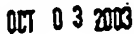
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Examiner Signature	<i>Harold Schulz</i>	Date Considered	3/16/06
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PTO/SB/08B (08-03)

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### NON PATENT LITERATURE DOCUMENTS

Examiner  
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Cite  
No. <sup>1</sup>

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

 $T^2$ 

KOGA ET AL., A 0.23  $\mu\text{m}^2$  Double Self-Aligned Contact Cell for Gigabit DRAMs With a Ge-Added Vertical Epitaxial Si Pad, 1996 IEEE, IEDM 96-589 - IEDM 96-592



HADA ET AL., A Self-Aligned Contact Technology Using Anisotropic Selective Epitaxial Silicon For Giga-Bit DRAMs, IEEE, 1995 IEDM 95-665 - IEDM 95-668

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Signature**

# hanna schulz

Date \_\_\_\_\_

**Considered**

3/16/06

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